

EASWARI ENGINEERING COLLEGE

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Student Association of ECE

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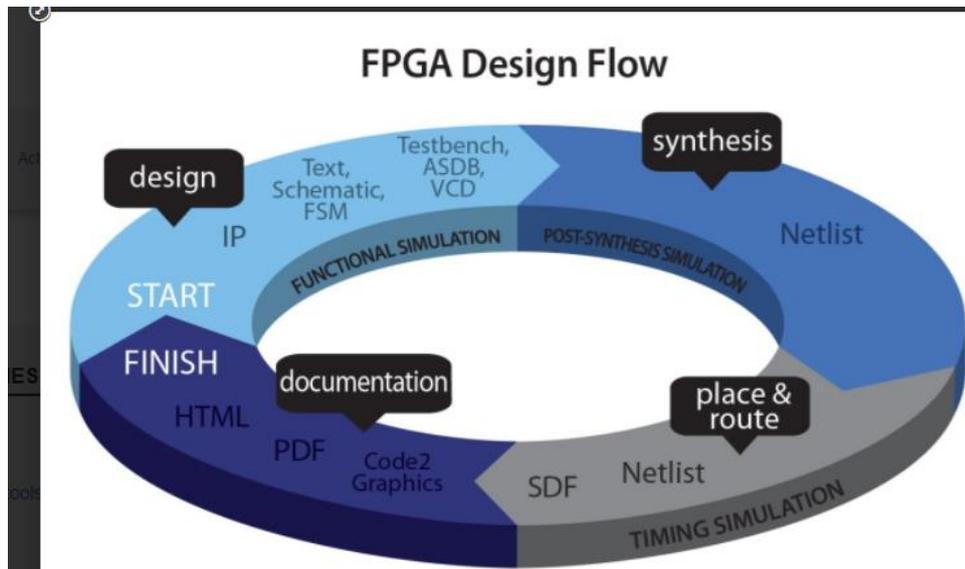
Guest Lecture – Trends and Opportunities in the VLSI Industry

**Guest Speaker: Mrs. Vidhya Natarajan,
Principal Engineer,
Microchip Technology, Chennai.**

The session started with the highlights of the guest's area of interest and her working environment. The session was highly interactive as the speaker was very enthusiastic and made the students to recollect the needed basics of Digital Logic Circuits and relate it to VLSI design techniques. She shot out questions, every now and then and made the session even livelier.



The semiconductor industry forecast for the year 2018 was made clear to the students. Overview of the chip design cycle was highly informative. The History and evolution of Moore's law was another part which attracted the audience a lot. She explained about the evolution of the semiconductor industry, dated back to transistors till where we stand today.



The FPGA design cycle was well explained and the students were made to imagine and visualize the different steps involved. The ASIC design flow and Physical design flow concepts too made the students relate to what they learn with the real scenario.

The VLSI industry and the opportunities available in the industry were communicated to the students. The guest speaker ran through the EDA tool development, infrastructure maintenance and application engineering; capturing the thoughts of the students. The tools in ASIC / FPGA design and verification were also introduced to the students. She addressed the students about the opportunities available for core industry placements and the path to be moved on, to catch hold of the offer.



On the whole, the session had different phases like the basic need to understand the VLSI industry, current trends, focus on building a thoughtful application and career guidance as well.

Faculty In-charge

HOD / ECE